

FIG. 1
(Prior Art)

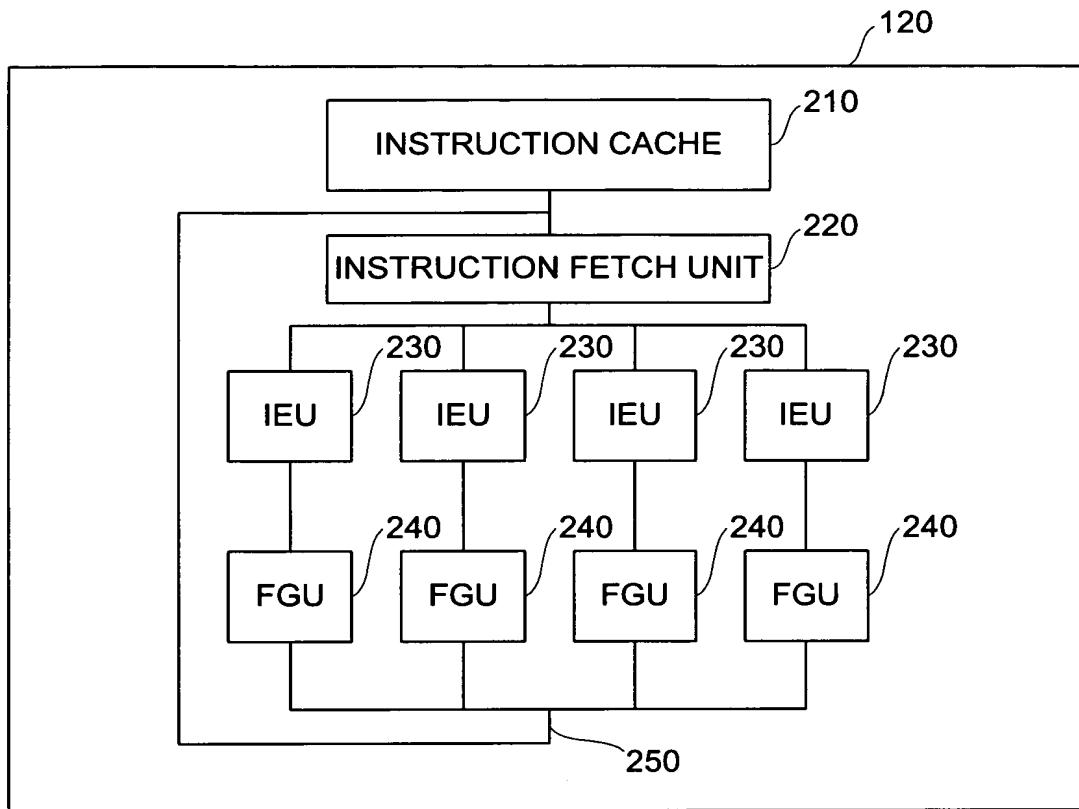


FIG. 2
(Prior Art)

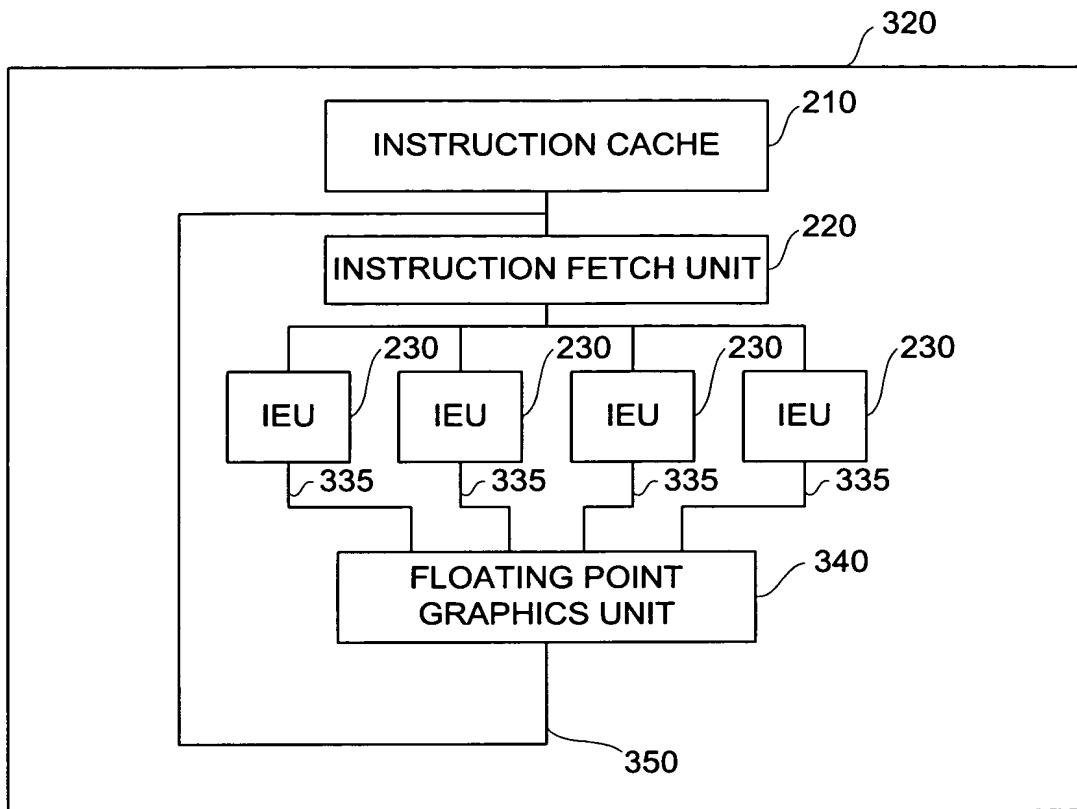


FIG. 3

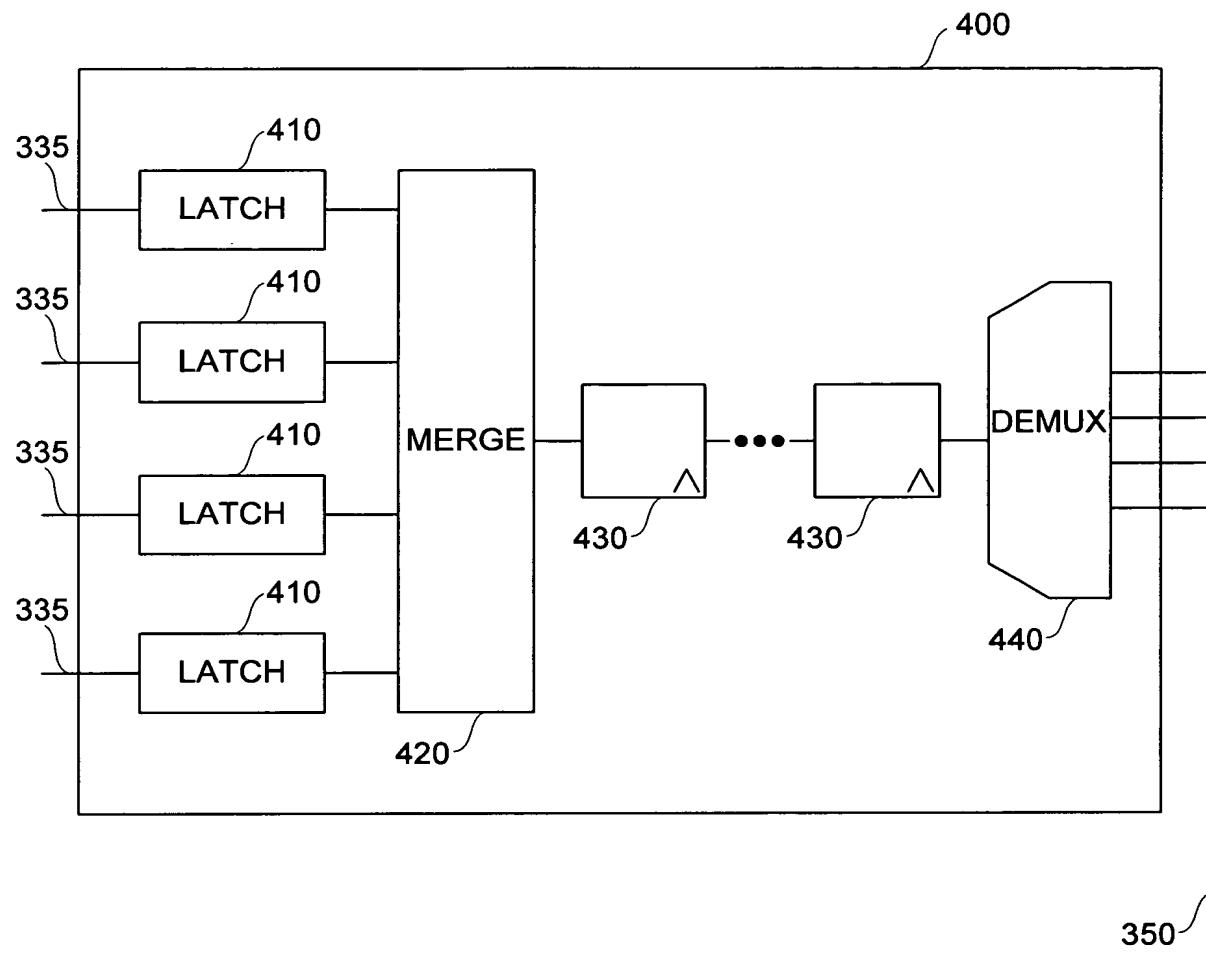


FIG. 4

Title: PROCESSOR CORE CLOCK GENERATION CIRCUITS

Inventor: XiuJun Guan

Application No: 10/735,104

Docket No: SUNMP348

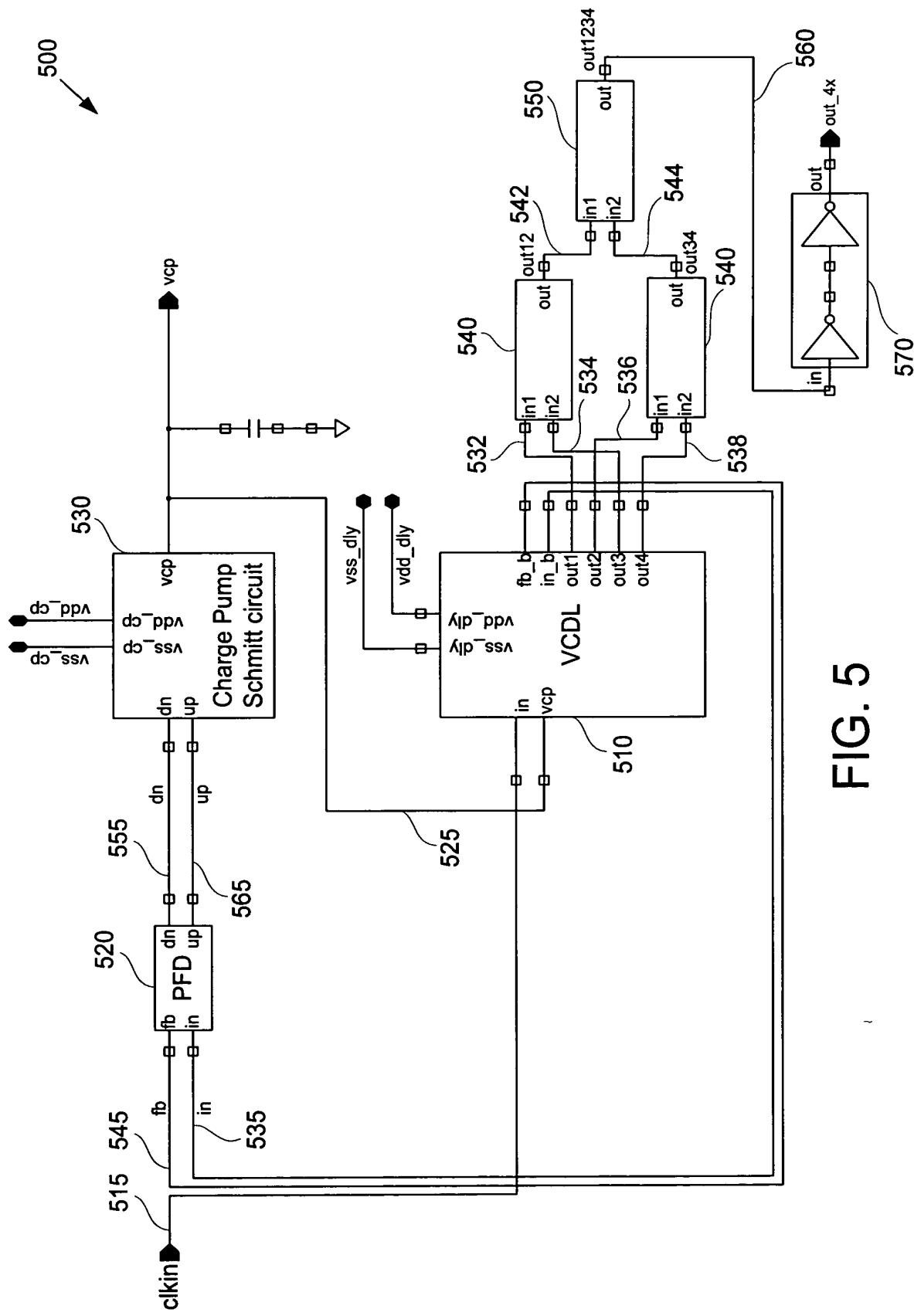


FIG. 5

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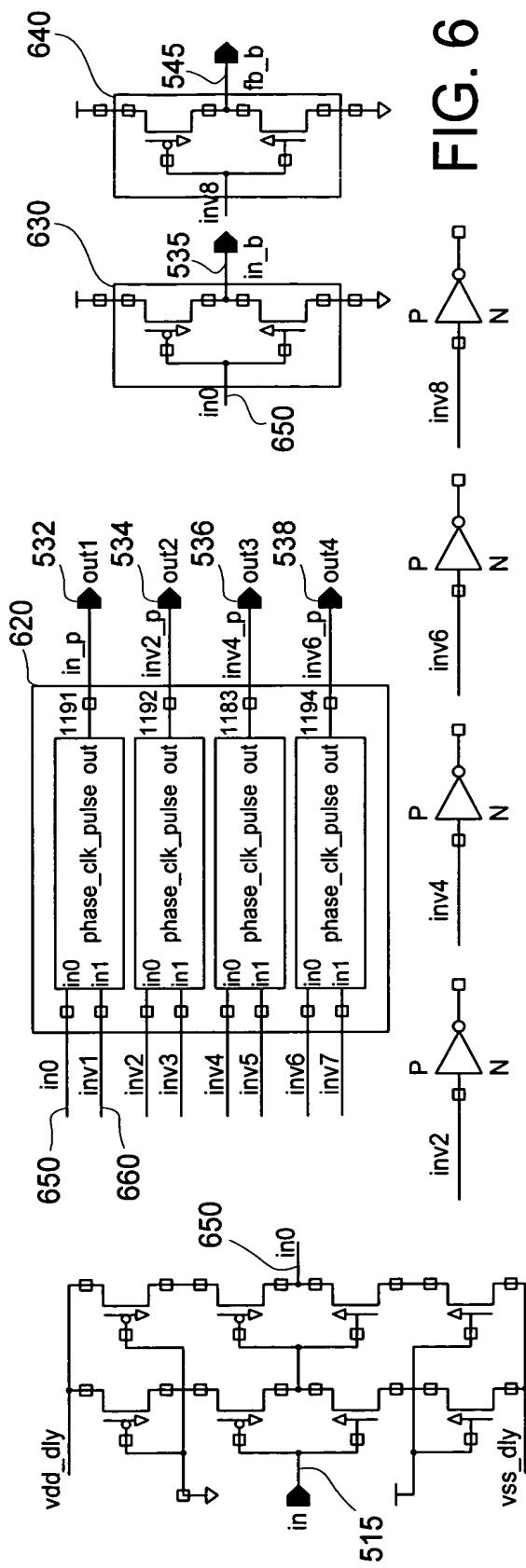
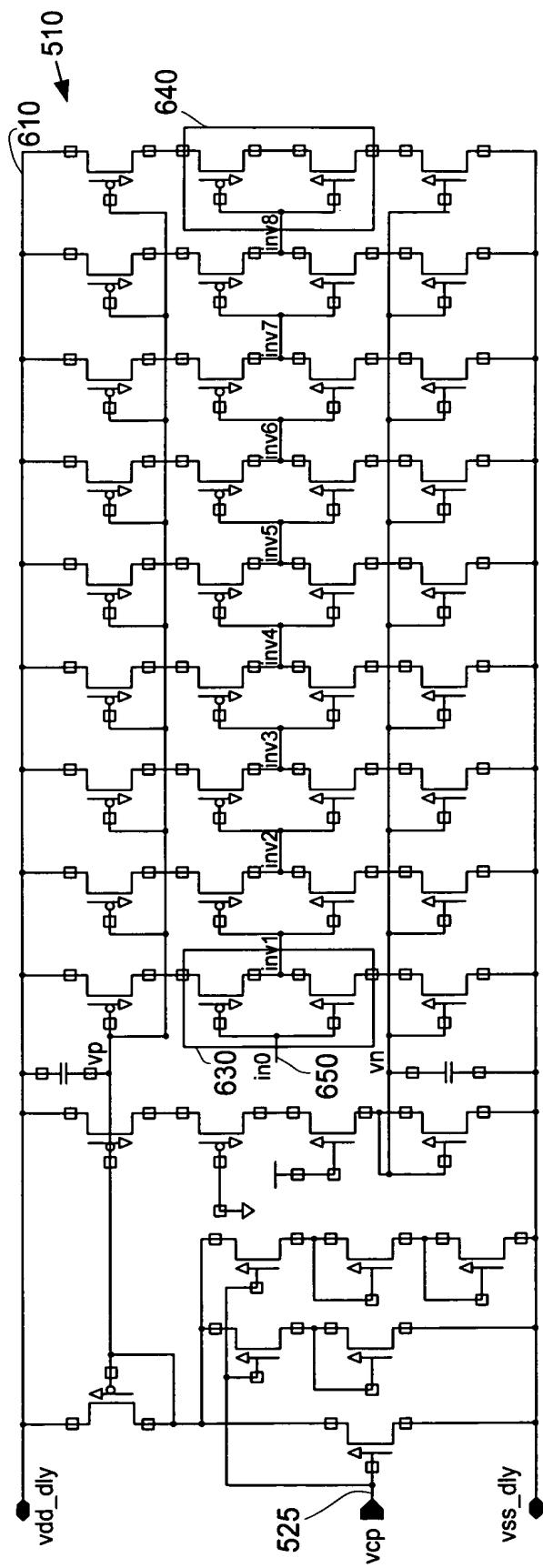


FIG. 6

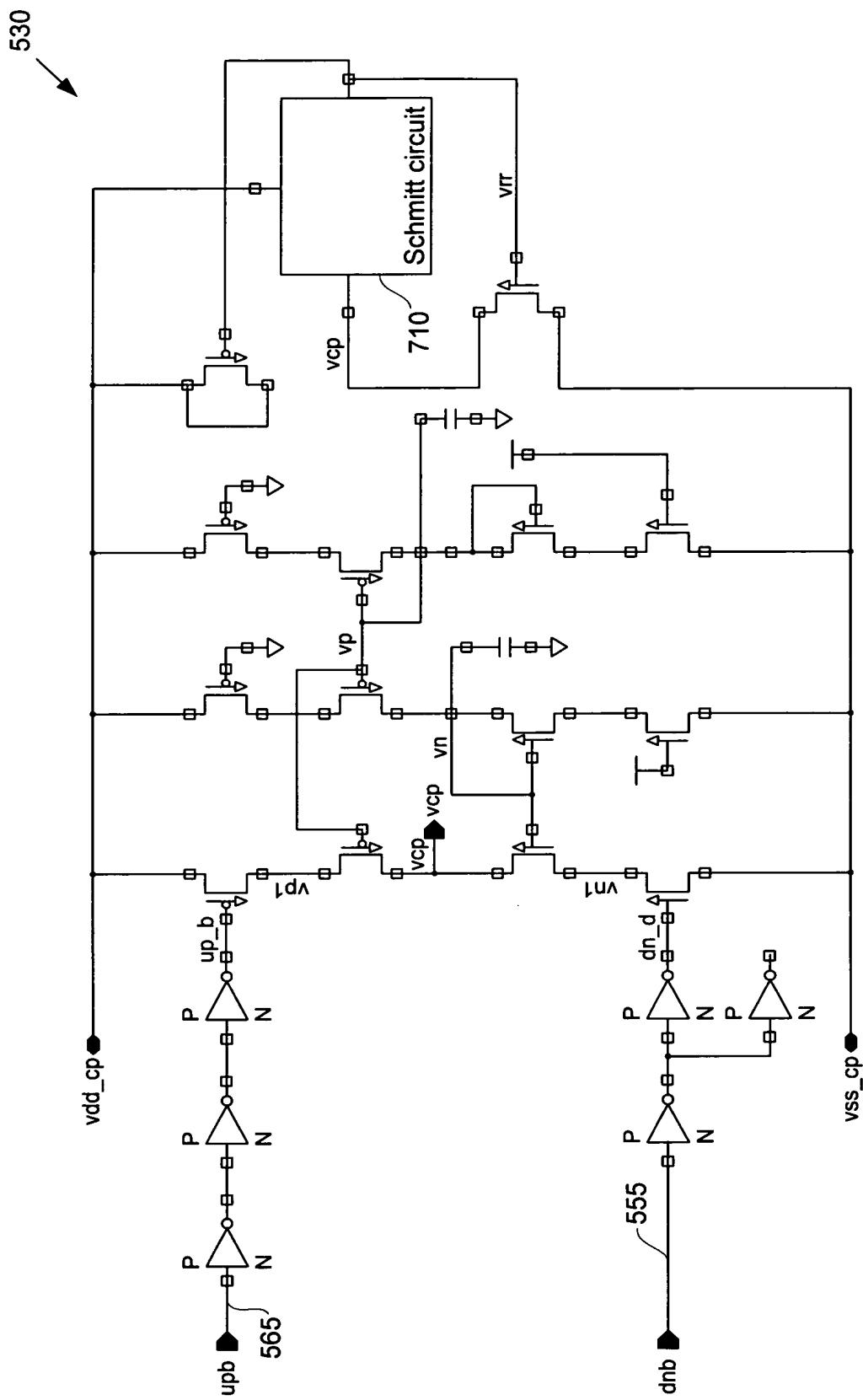


FIG. 7A

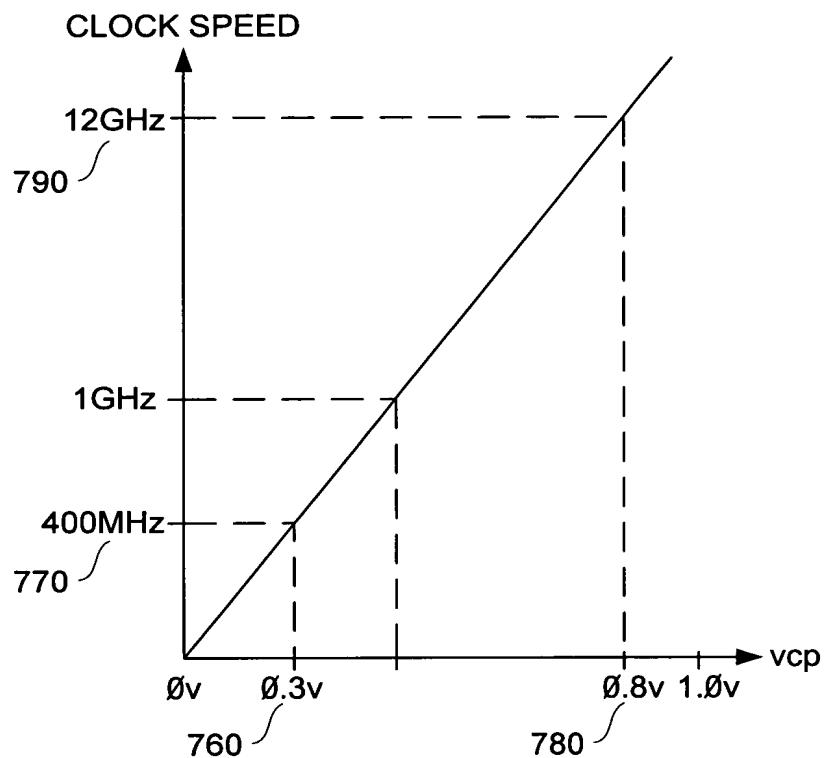


FIG. 7B

700

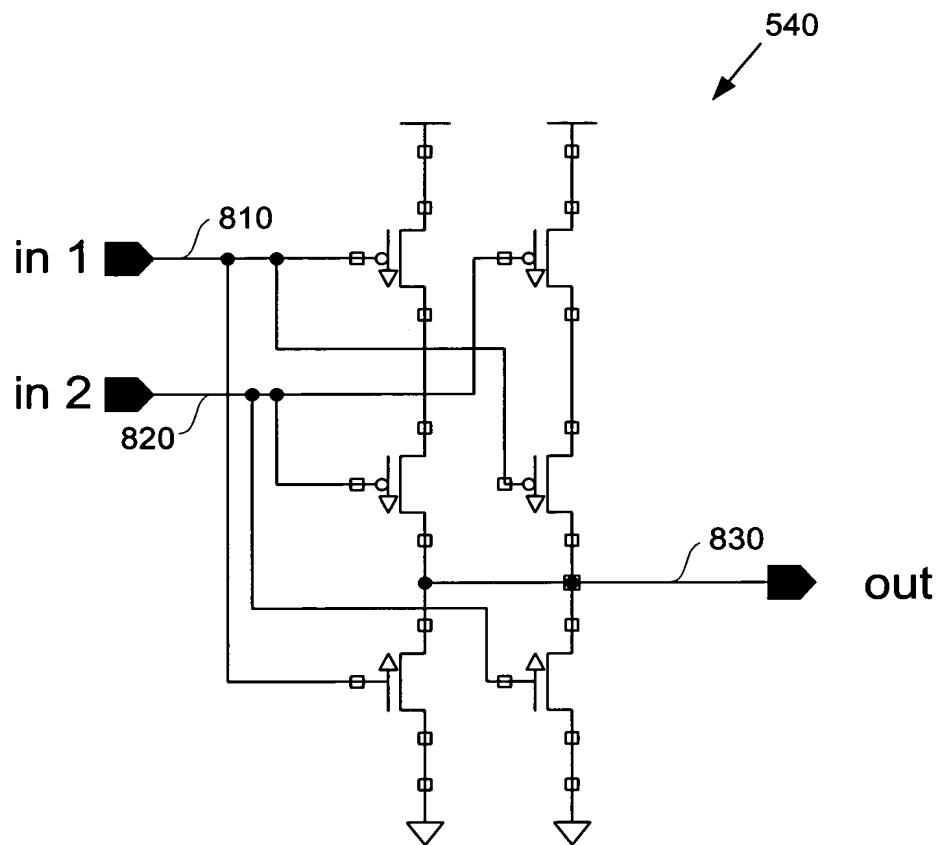


FIG. 8

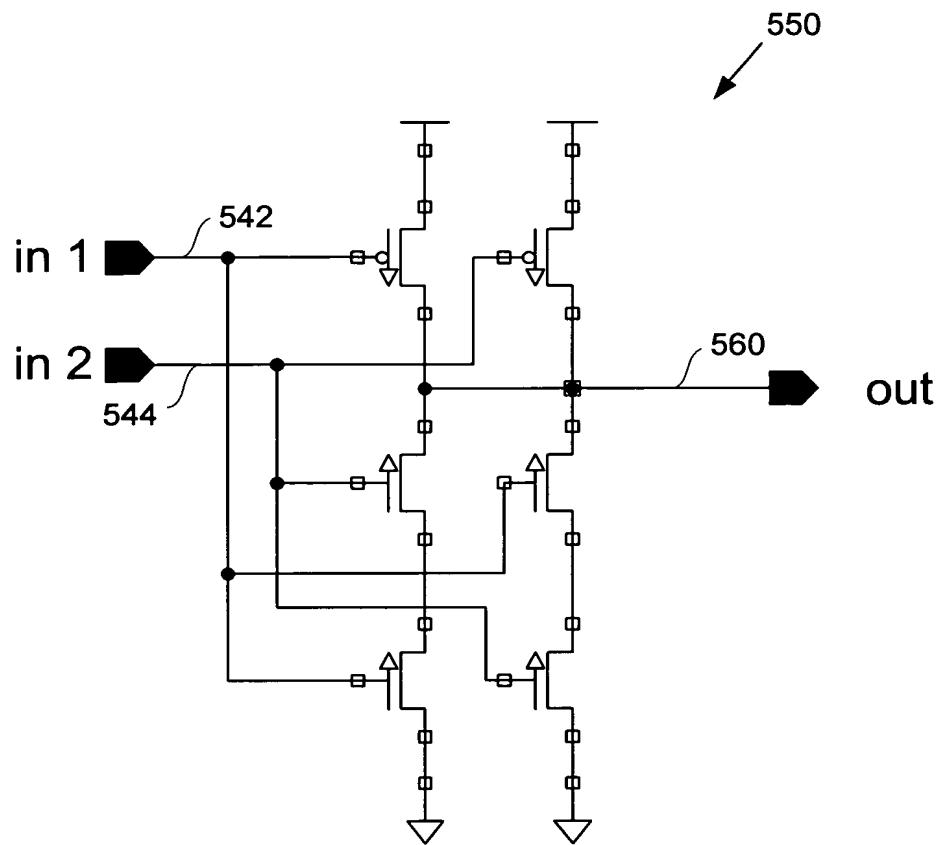


FIG. 9

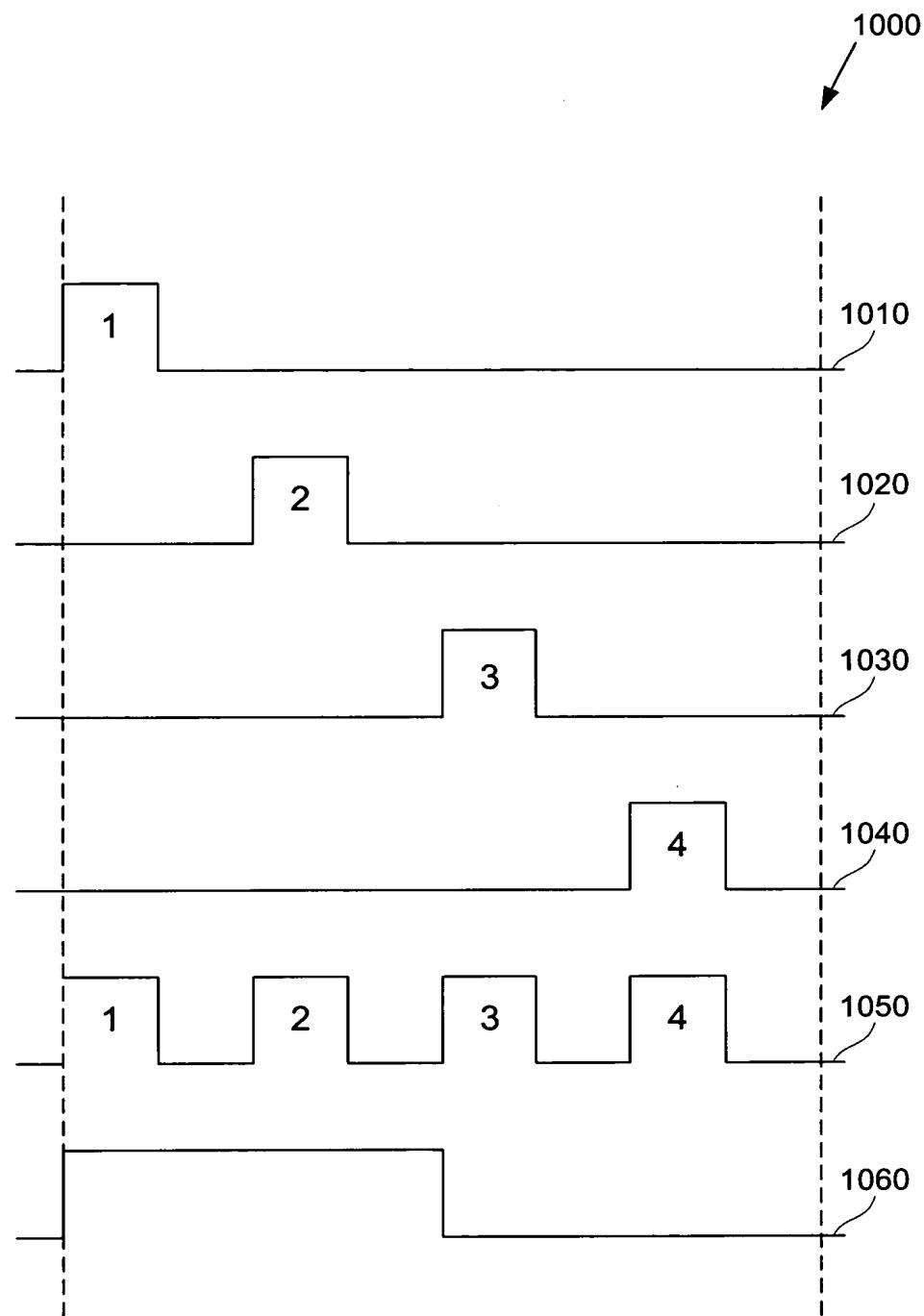


FIG. 10

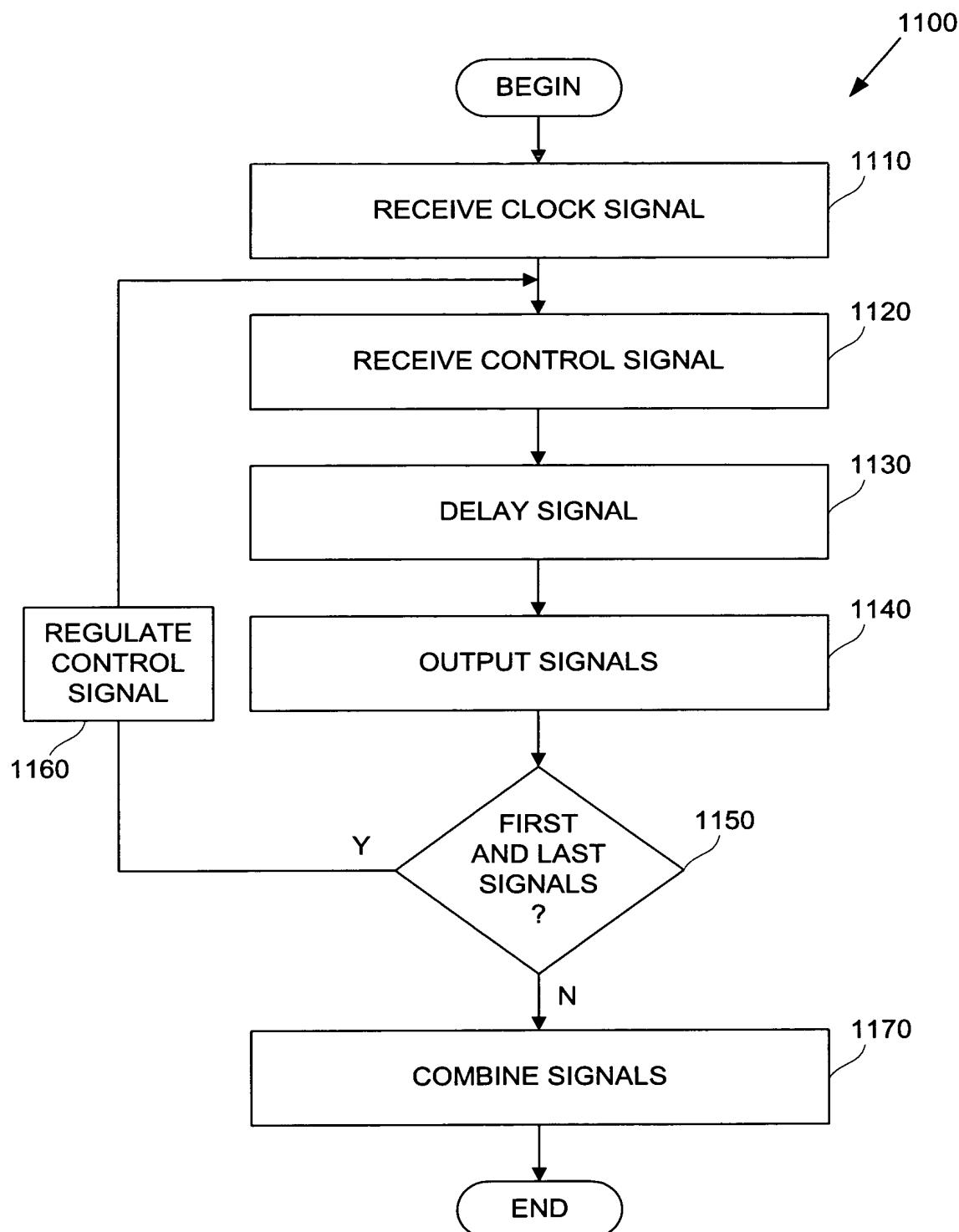


FIG. 11